



AT32-Ethernet-SUB-IP101GR V1.0

LED1



R2

H1

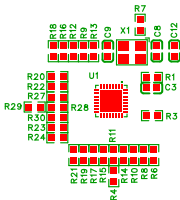


3V3

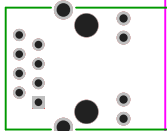
H2



GND



U2



CN1

B1



PHY_RST

GND

3V3

INT

TXD3

CLK_IN

TXD1

TX_EN

RXD3

RXD1

RX_DV

MDIO

CRS

TXD2

GND

3V3

RST

TXD0

RXER

RXD2

RXD0

COL

RXC

TXC

MDC

L1
C4
C5
C7
C6

R5
C1
C2