

#### **MG0012**

Migration Guide

### Migrating from AT32F403 to AT32F413

## Introduction

This migration guide is written to help users with the analysis of the steps required to migrate from an existing AT32F403 series to AT32F413 series device. It puts together the most important information and lists the vital aspects that need to be taken into account.

To move an application from AT32F403 series to AT32F413 series, users have to analyze the hardware and software migration.

Applicable products:

Part numbers AT32F413xx



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# 1 Similarities and differences between AT32F413 and AT32F403

The AT32F413 series microcontrollers are basically compatible with the AT32F403 series and provide many enhanced features, except for minor differences from the AT32F403 series. The differences between them are detailed in this document.

## 1.1 Overview of similarities

- Pin definition: The same package has the same pin definition. For extended peripherals, the alternate function of pins are defined.
- Addressing space: Memory and registers have the same logical addresses, except I2C3 & CAN2. Extended peripherals occupy the reserved space of AT32F403 series.
- Compiler tools: identical, for example, Keil, IAR

## 1.2 Overview of differences

	AT32F413 AT32F403					
Startup	8 ms	20 ms				
Reset	3.6 ms	8.2 ms				
Wake up from	3.6 ms	150 ms				
Standby mode	5.0 ms					
Main memory size	Up to 256 KB	Up to 1024 KB				
SRAM size	Extended up to 64 KB	Extended up to 224 KB				
Flash memory 16-	F0.00	30 µs				
bit write time	50 µs					
Flash memory	50 ms (AT32F413xC)	40				
sector erase time	40 ms (AT32F413xB/x8)	40 ms				
	800 ms	5s (AT32F403xC)				
Flash memory mass		10s (AT32F403xE)				
erase time		20s (AT32F403xG)				
Security library	Support	NA				
Run mode	28.4 mA @ 72 MHz	33.7 mA @ 72 MHz				
Power consumption		24.7 mA @ 72 MHz				
at Sleep mode	23.9 mA @ 72 MHz					
Power consumption	4004	1 mA				
at Deepsleep mode	490 uA					
Power consumption	9.9 uA	10.4uA				
at Standby mode	9.9 uA					
VBAT independent	Support	NA				
supply						

#### Table 1. Differences between AT32F413 and AT32F403



## 2 Hardware migration

The migration from AT32F403 to AT32F413 series is very simple as they are pin-to-pin compatible basically.





## **3** Software migration

## 3.1 Functional enhancement

This section describes the enhanced peripheral features of AT32F413 versus AT32F403. The subsection presents the behavior of the AT32F413.

## 3.1.1 High frequency PLL settings

- AT32F413 has an auto clock frequency switch feature, making it a bit different from AT32F403 in terms of clock configuration procedure.
- For AT32F403, software latency is needed to wait until the HEXT and PLL become stable. This step can be skipped for AT32F413, for it has been guaranteed by hardware.
- When the embedded PLL is greater than 108 MHz in AT32F413, the PLL settings are a bit different as AT32F413 needs use auto clock frequency switch feature.

#### 3.1.2 Internal memory size extension

• The internal memory extension features supported by AT32F413 are different from those of AT32F403. For AT32F413 series, the internal memory can be 16 KB, 32KB or 64 KB.

#### 3.1.3 Security library

• Security library is supported.

## 3.1.4 DMA flexible mapping request feature

• DMA1/DMA2 has flexible mapping request feature each.



# 4 Revision history

Table 2. Document revision history

Date	Revision	Changes	
2022.02.28	2.0.0	Initial release	
2022.10.19	2.0.1	Added 3.1.4 DMA flexible mapping request feature	

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