

FAQ0100

Frequently Asked Questions

How to clock AT32F407/437 Ethernet PHY?

Questions:

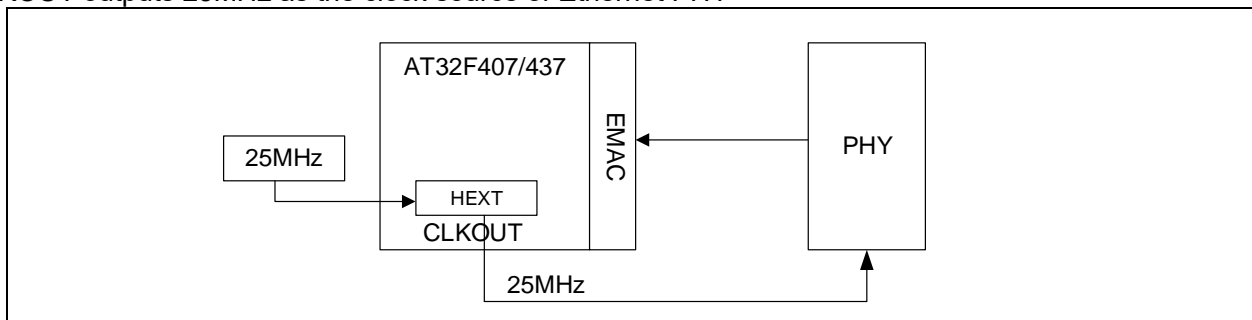
How to clock Ethernet PHY in AT32F407/437 series?

Answer:

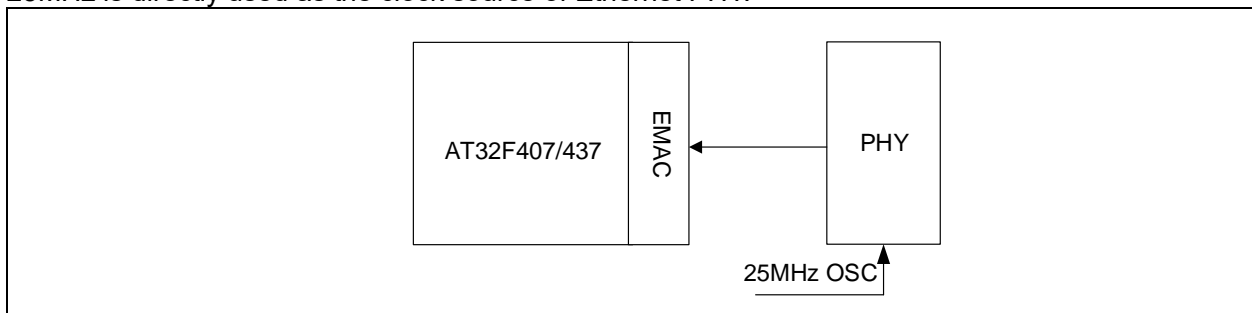
If EMAC is to be enabled in the AT32F407/437 series, it is necessary to provide 25MHz/50MHz clock to Ethernet PHY.

Proceed as follows:

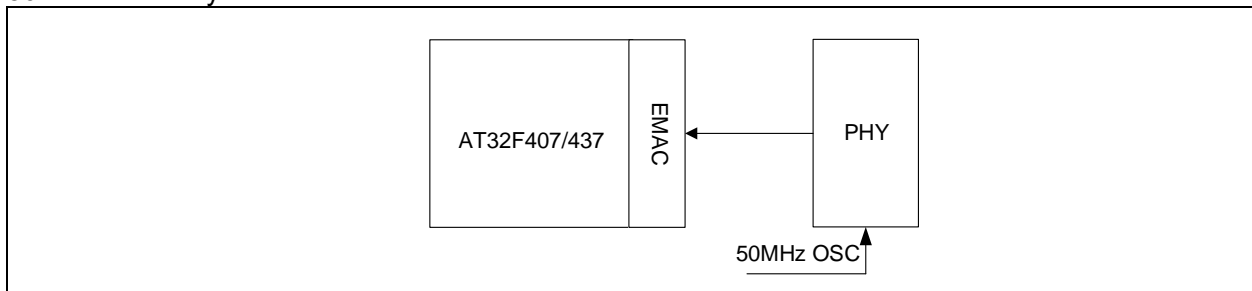
- For 25MHz clock to the Ethernet PHY, follow method 1 and 2 below;
 - For 50MHz clock to the Ethernet PHY, follow method 3 (an external 50 crystal oscillator is recommended)
1. Select 25MHz crystal oscillator as HEXT of AT32F407, select HEXT as CLKOUT(PA8) clock source, and CLKOUT outputs 25MHz as the clock source of Ethernet PHY



2. 25MHz is directly used as the clock source of Ethernet PHY.



3. 50MHz is directly used as the clock source of Ethernet PHY.



Type: MCU applications

Applicable products: AT32F407/437

Main function: EMAC

Minor function: None

Document revision history

Date	Revision	Changes
2022.2.24	2.0.0	Initial release
2022.11.11	2.0.1	Updated how to select a clock source for Ethernet PHY? Added a description of how to provide clock for AT32F437 Ethernet PHY.

